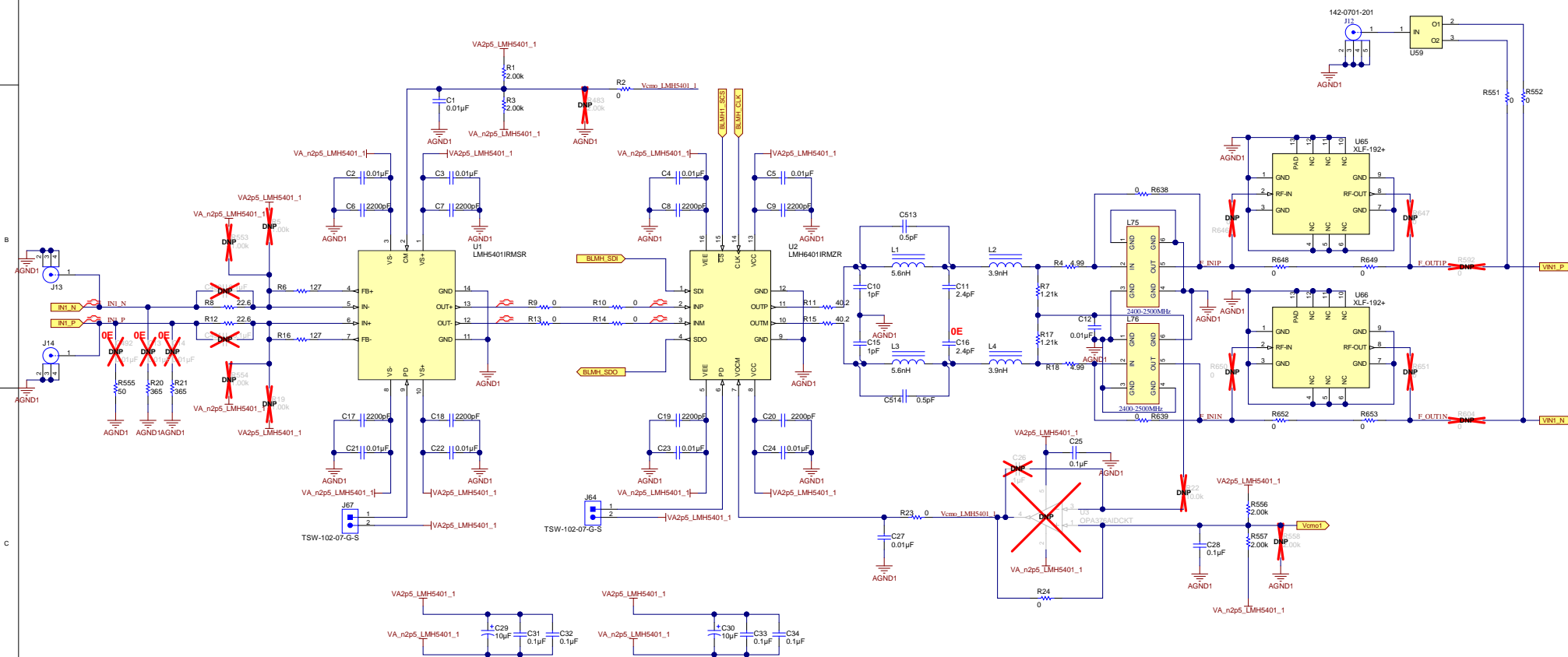


Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

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Orderable: NA	Designed for: Public Release	Mod. Date: 5/15/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128	Rev: E3	Sheet Title: Block Diagram
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A_Orderable	Sheet: 1 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_CoverSheet_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	


ANALOG INPUT CH-1



Note:

- 1) Replace capacitor (C492, C13, C14) with 0E resistor for DC coupling
50 E impedance

Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Build on: NDA-01022-E3 PCB and power solution used from NDA-01027		Designed for: Public Release		[Mod. Date: 3/18/2019]	
Order #: NA		Project Title: TIDA-01028		 TEXAS INSTRUMENTS	
TID # 010128		Sheet Title: ADC ANALOG INP CH1			
Number: TIDA-01028 Rev: 53		Assembly Variant: TIDA-01022A Orderable			
S/N Key: Version control disabled		Sheet: 2 of 26			
Drawn by: Avnash N		File: TIDA-01022-E3_ADC ANALOG INP_CH1_Sch.Dwg Size: B		http://www.ti.com	
Engineer: Anbu Mani		Contact: http://www.ti.com/support		© Texas Instruments 2017	

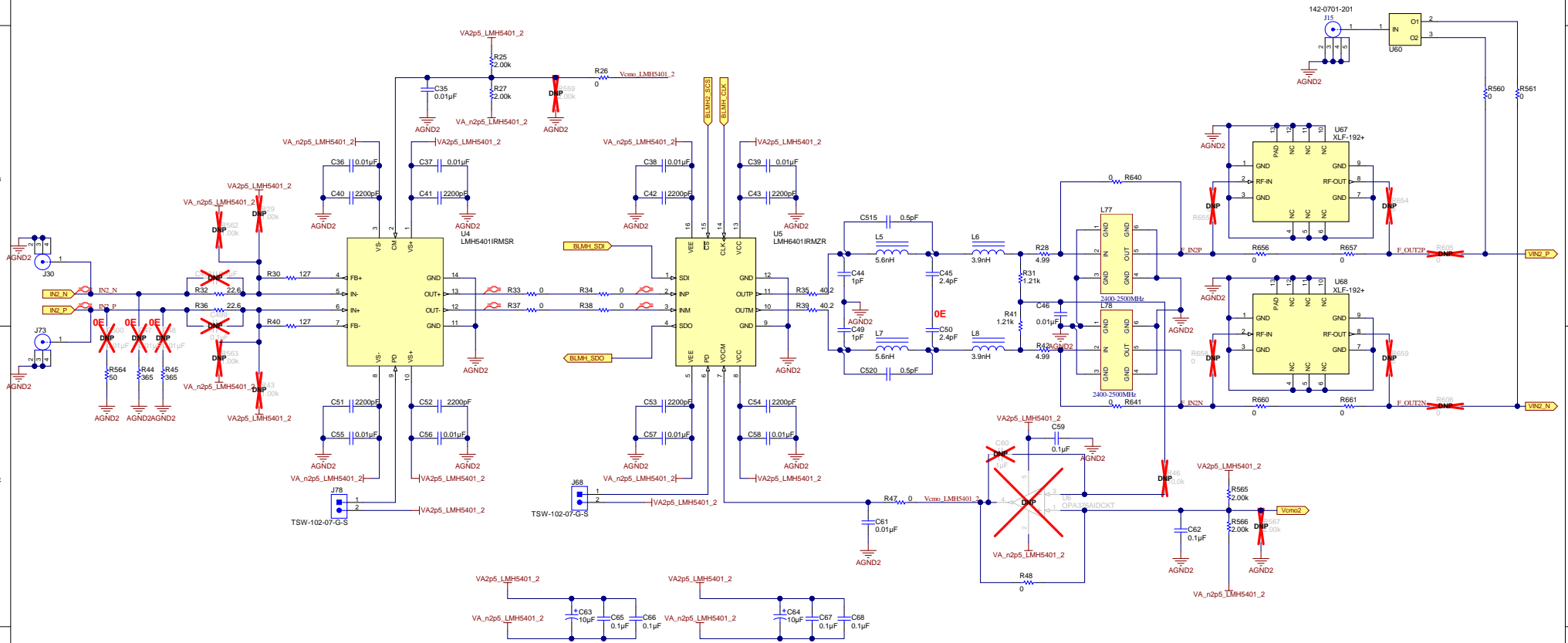
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1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

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Orderable: NA	Designed for: Public Release	Mod. Date: 5/8/2018
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128	Rev: E3	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A, Orderable	Sheet: 2 of 3
Drawn By:	File: Sheet1.SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	

ANALOG INPUT CH-2



- Note:
- 1) Replace capacitor (C500, C47, C48) with 0E resistor for DC coupling 50 E impedance

Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

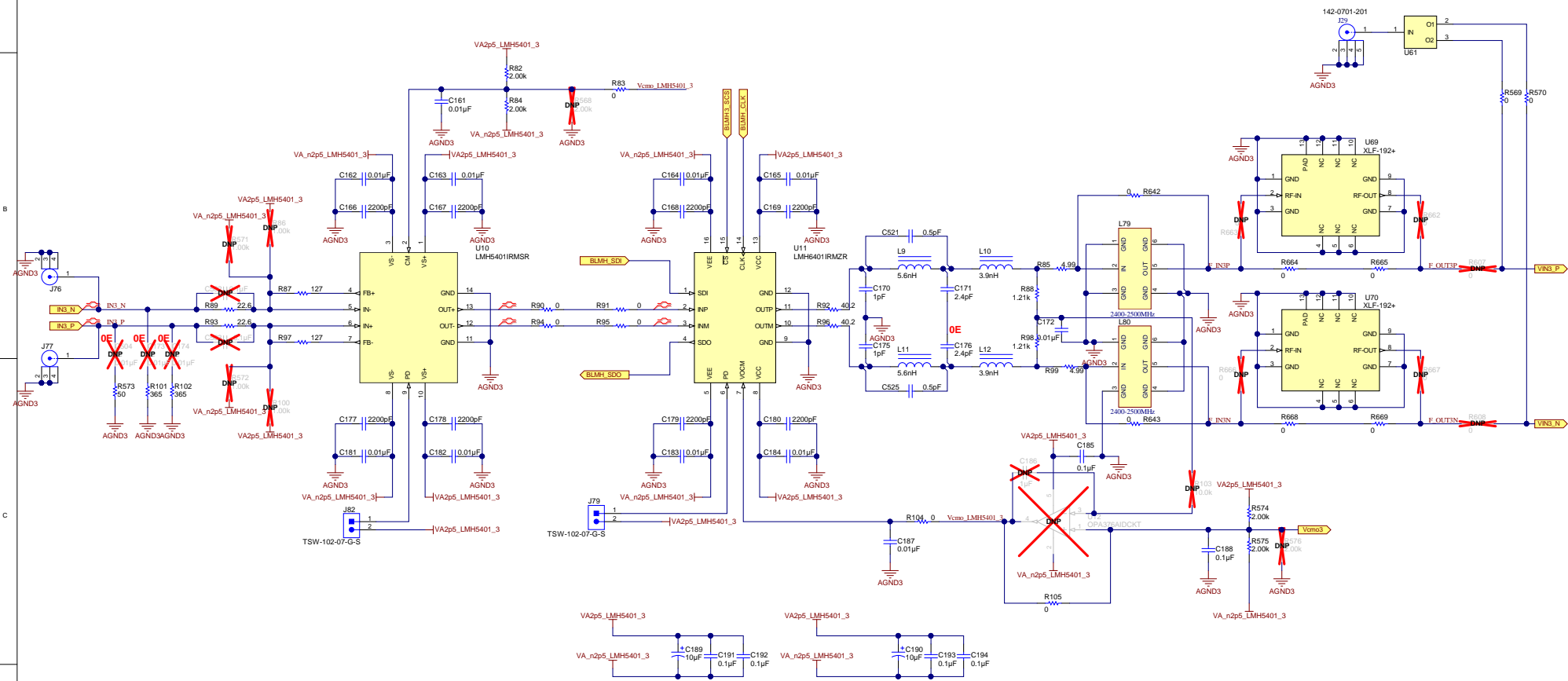
Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID # 010128	Project Title: TIDA-010128	
Number: TIDA-010128 Rev. E3	Sheet Title: ADC-TID-010128 CH2	
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A Orderable	Sheet: 3 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_ADC_ANALOG_INP_CH2_Sch.Dwg	Size: B
Engineer: Arbu Mala	Contact: http://www.ti.com/support	



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ANALOG INPUT CH-3



Note:

- 1) Replace capacitor (C504, C173, C174) with 0E resistor for DC coupling
50 E impedance

Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

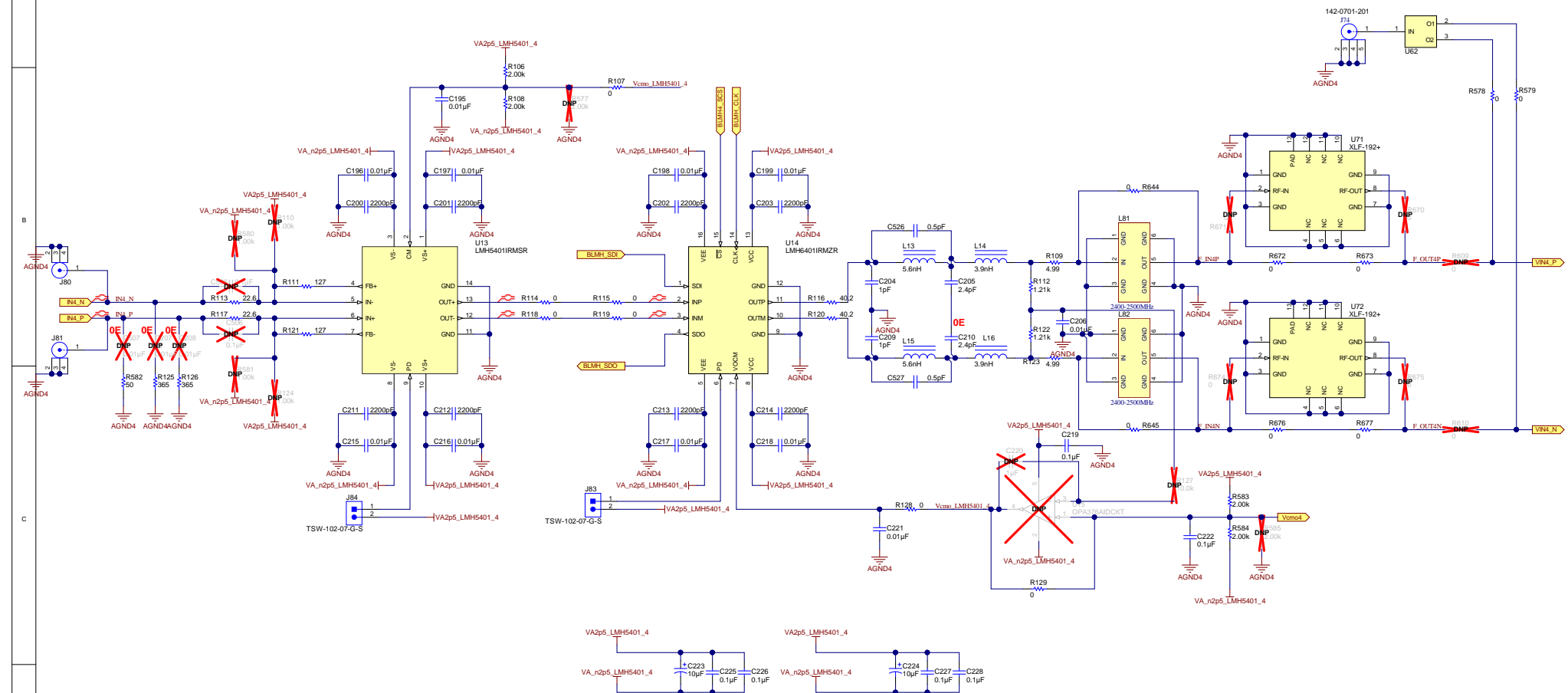
Order on: **TIDA-010218** **ESD and power solution used from TIDA-010218**
 Designed for: **Public Release** **Mod. Date: 3/5/2019**
 TID A: **010128** **Project Title: TIDA-010128**
 Number: **TIDA-010128** **Rev: E3** **Sheet Title: ADC12D060INP_CH3**
 SVN Ref: **Version control disabled** **Assembly Variant: TIDA-01022A** **Orderable** **Sheet: 4 of 26**
 Drawn By: **Avinash N** **File: TIDA-01022-ES3_ADC_ANALOG_INP_CH3_SchDwg** **Size: B**
 Engineer: **Anbu Mani** **Contact: http://www.ti.com/support** **http://www.ti.com**
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ANALOG INPUT CH-4



Note:

- 1) Replace capacitor (C507, C207, C208) with 0E resistor for DC coupling
50 E impedance

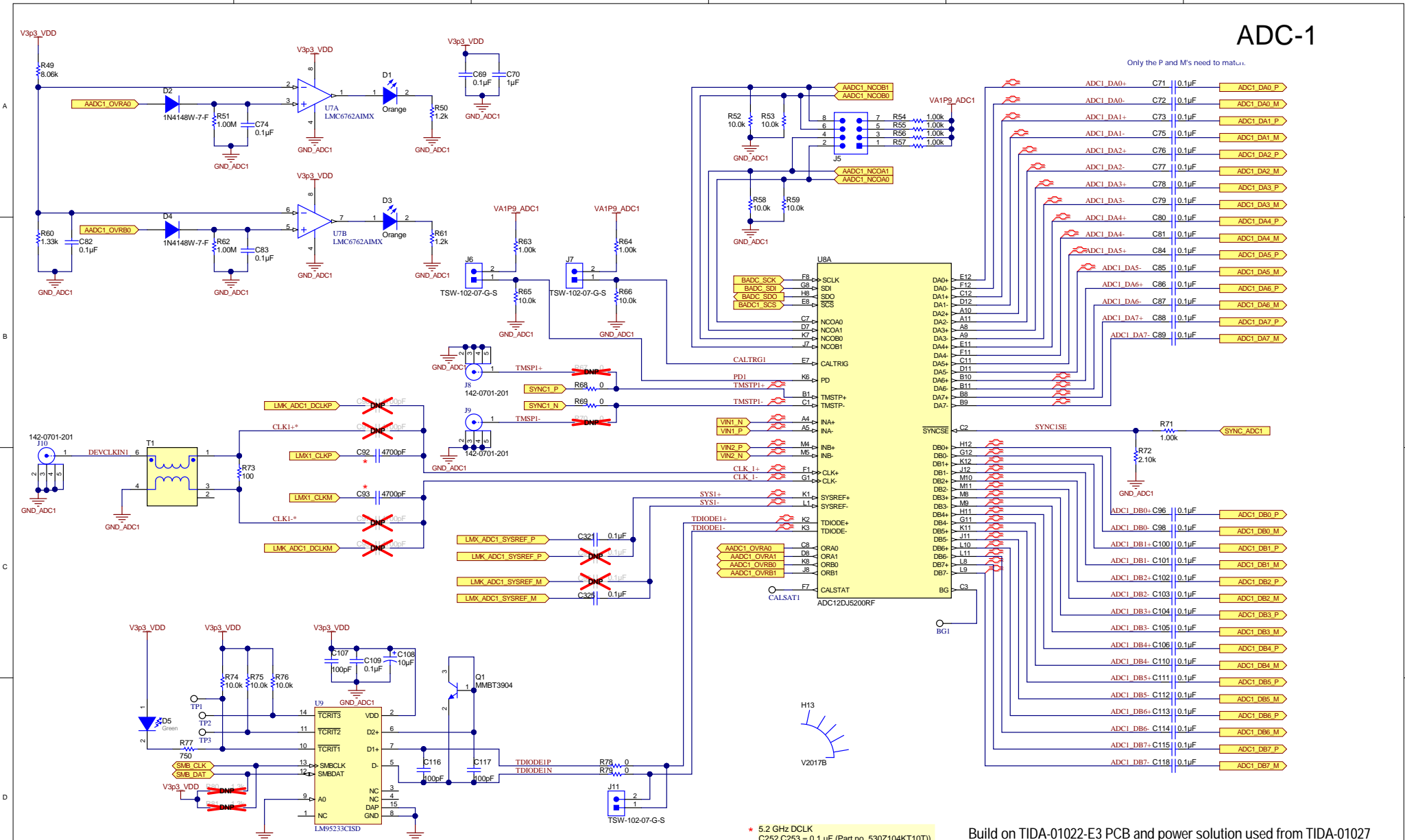
Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Build on: [TIDA-01022-E3 PCB and power solution used from TIDA-01027](#)
 Ordered: [NA](#) Designed for: [Public Release](#) [Mod. Date: 3/5/2019]
 TID # 010128
 Number: [TIDA-010128](#) [Rev. 53]
 SVN: RTR - Version control disabled
 Drawn by: [Avinash N](#) File: [TIDA-01022A_3 ADC ANALOG INP CH4 Sch.Dwg](#) Sheet: 5 of 26
 Engineer: [Anbu Mani](#) Contact: [http://www.ti.com/support](#) © Texas Instruments 2017

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ADC-1

Only the P and M's need to match.



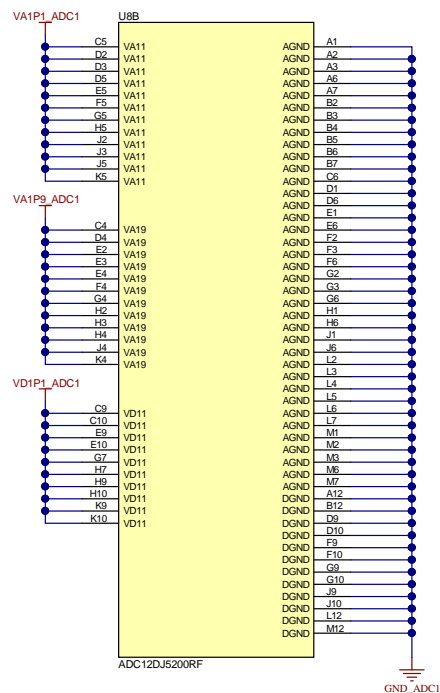
★ 5.2 GHz DCLK
C252, C253 = 0.1 uF (Part no. 530Z104KT10T)

Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027			
Orderable: NA	Designed for: Public Release	Mod. Date: 5/15/2019	
TID #: 010128	Project Title: TIDA-010128		
Number: TIDA-010128 Rev: E3	Sheet Title: ADC12DJ5200F	Orderable	Sheet: 6 of 26
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A3	Orderable	
Drawn By: Avinash N	File: TIDA-01022-E3_ADC12DJ5200F_1.SchDoc	Size: B	
Engineer: Anbu Mani	Contact: http://www.ti.com/support		

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ADC-1



Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable: NA	Designed for: Public Release	Mod. Date: 3/18/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128 Rev: E3	Sheet Title: ADC12DJ5200 1PWR	
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A_Orderable	Sheet: 7 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_ADC12DJ5200_1PWR_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	

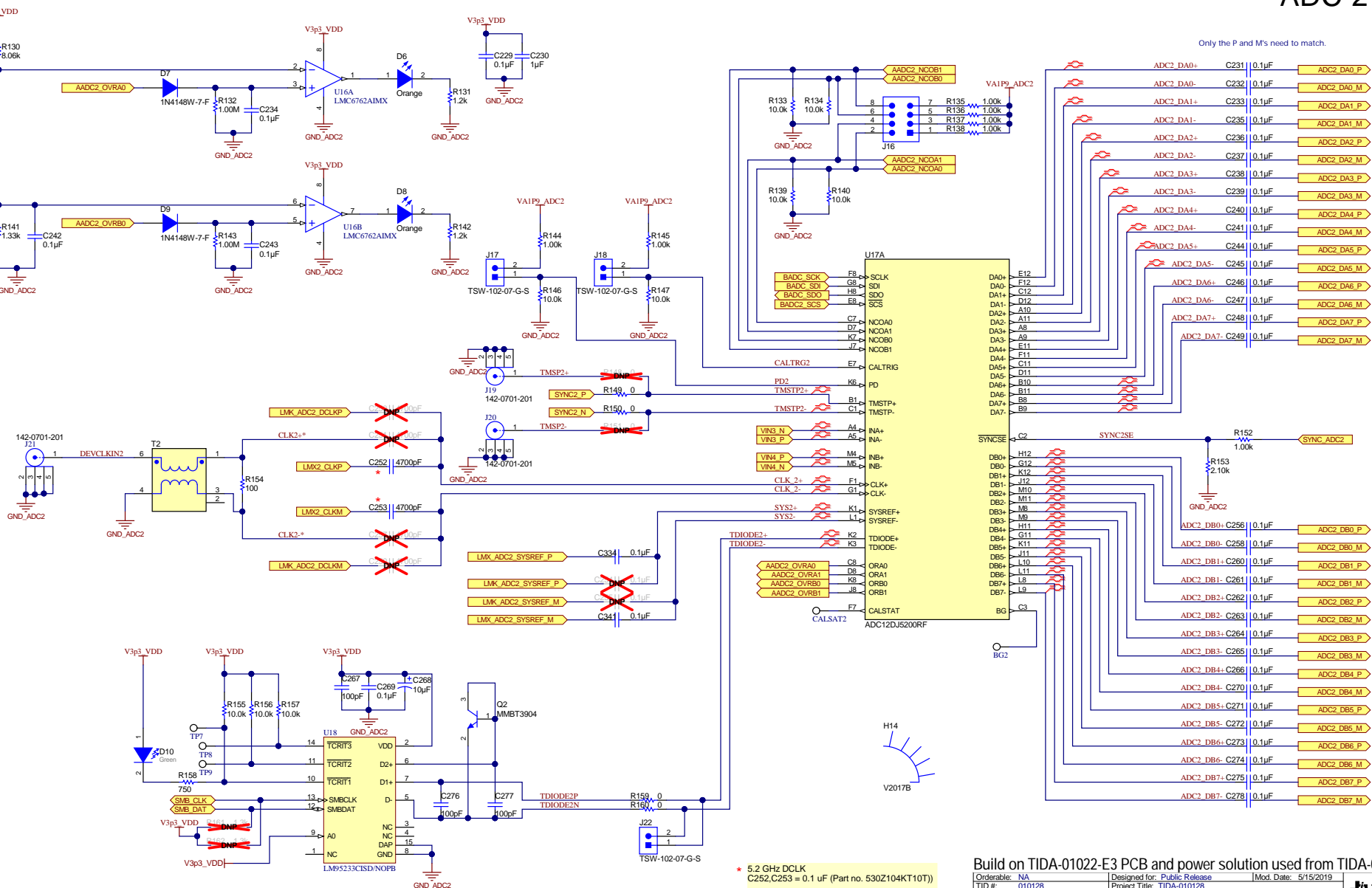


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ADC-2

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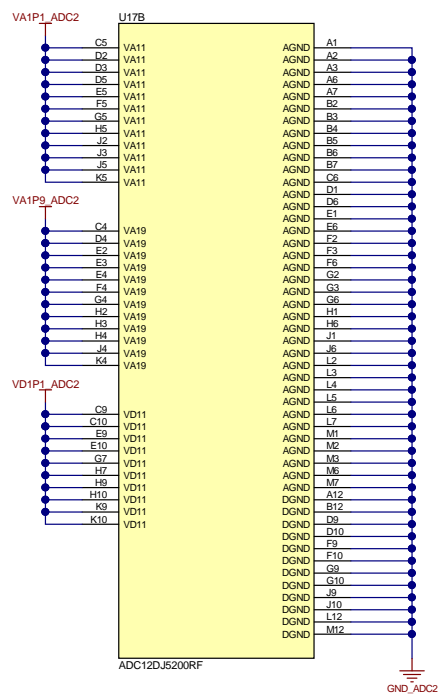
Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable: NA	Designed for: Public Release	Mod. Date: 5/15/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128 Rev: E3	Sheet Title: ADC12DJ5200_2	Sheet: 8 of 26
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A Orderable	
Drawn By: Avinash N	File: TIDA-01022-E3 ADC12DJ5200_2 SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	




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ADC-2



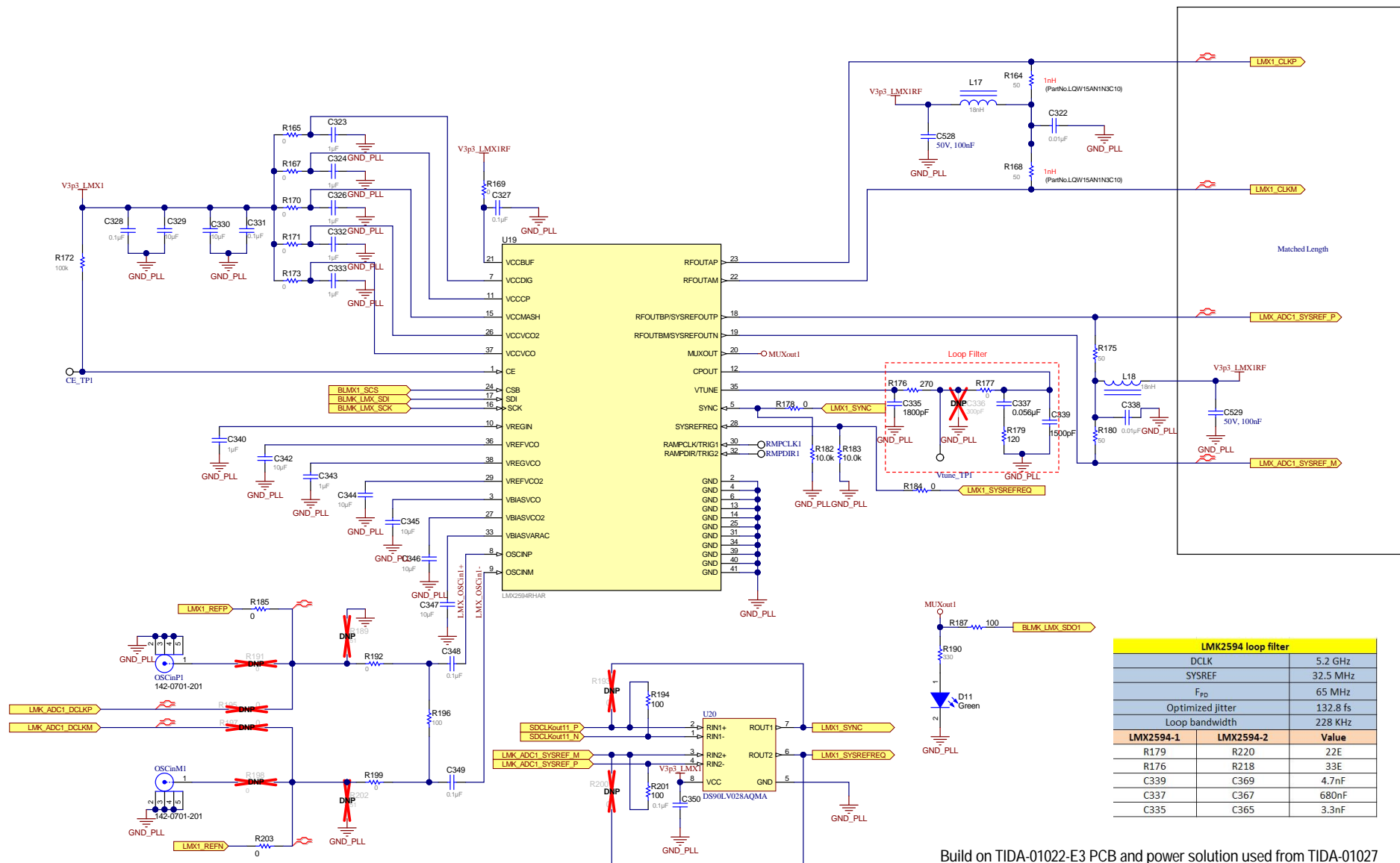
Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable NA	Designed for: <u>Public Release</u>	Mod. Date: 3/18/2019	 TEXAS INSTRUMENTS
TID #: <u>010128</u>	Project Title: <u>TIDA-010128</u>		
Number: <u>TIDA-010128</u> [Rev. <u>E3</u>	Sheet Title: <u>ADC12DJoxx00_1PWR</u>		
SVN Rev: Version control disabled	Assembly Variant: <u>TIDA-010128A Orderable</u>	Sheet: <u>9</u> of <u>26</u>	
Drawn by: <u>Avishav N</u>	File: <u>TIDA-01022-E3_ADC12DJoxx00_2PWR.SchDoc</u>	Size: <u>B</u>	
Engineer: <u>Anbu Mani</u>	Contact: <u>www.ti.com</u>	http://www.ti.com	© Texas Instruments 2017



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LMX2594 -1



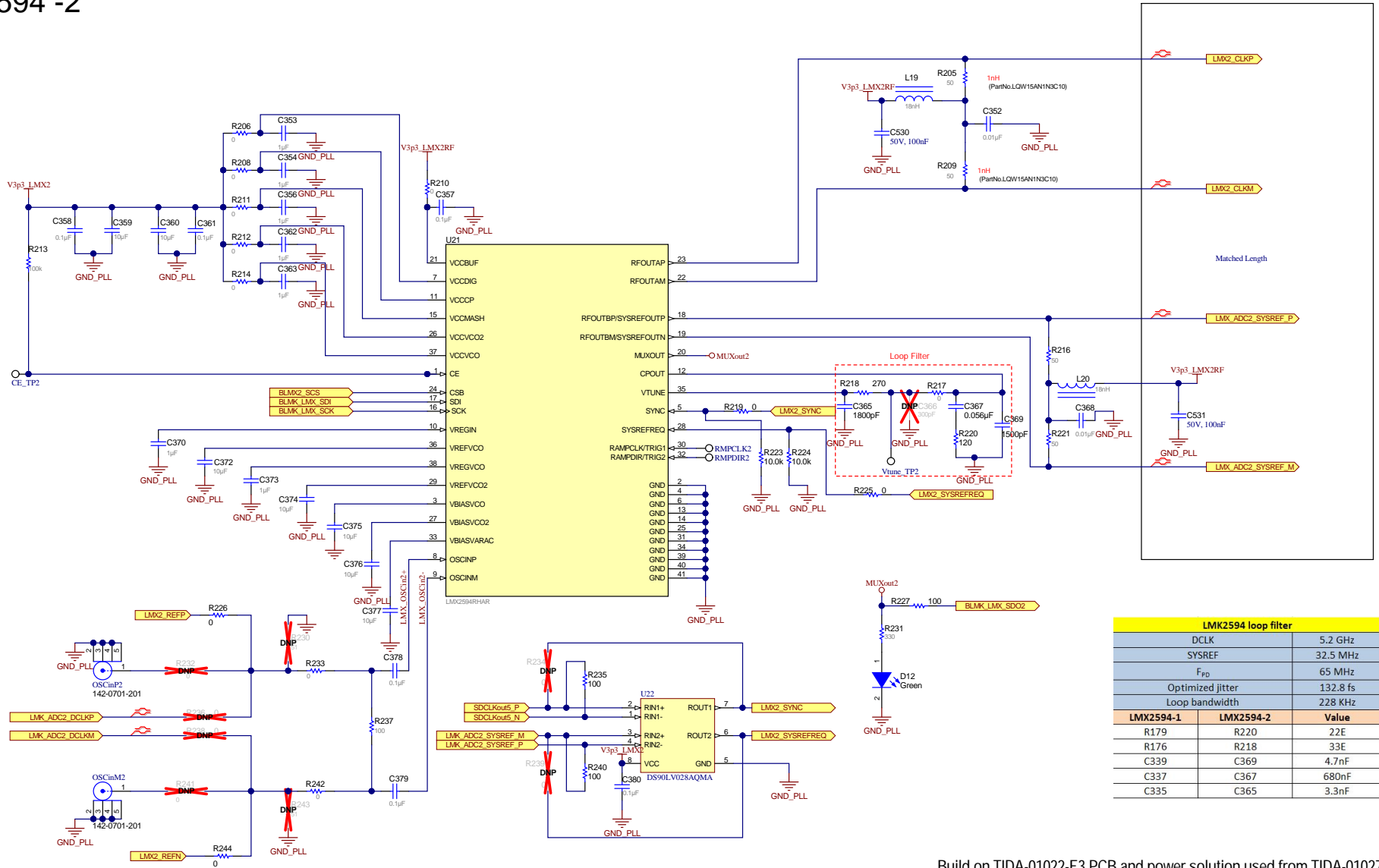
Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

LMX2594 loop filter		
DCLK		5.2 GHz
SYSREF		32.5 MHz
F _{PD}		65 MHz
Optimized jitter		132.8 fs
Loop bandwidth		228 KHz
LMX2594-1	LMX2594-2	Value
R179	R220	22E
R176	R218	33E
C339	C369	4.7nF
C337	C367	680nF
C335	C365	3.3nF

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Orderable NA		Designed for: <u>Public Release</u>		Mod. Date: 5/15/2019	
TID #: <u>010128</u>		Project Title: <u>TIDA-010128</u>			
Number: <u>TIDA-010128</u> Rev: <u>E3</u>		Sheet Title: <u>LMX2594R</u>			
SVN Rev: Version control disabled		Assembly Variant: <u>TIDA-010224</u> Orderable		Sheet: <u>10</u> of <u>26</u>	
Drawn by: <u>Avinash N</u>		File: <u>TIDA-010224-E3_LMX2594R_1_SchDoc</u>		Size: B	
Engineer: <u>Anbu Mani</u>		Contact: <u>http://www.ti.com/support</u>		http://www.ti.com © Texas Instruments 2017	

LMX2594 -2



LMX2594 loop filter		
DCLK		5.2 GHz
SYSREF		32.5 MHz
F _{PD}		65 MHz
Optimized jitter		132.8 fs
Loop bandwidth		228 KHz
LMX2594-1	LMX2594-2	Value
R179	R220	22E
R176	R218	33E
C339	C369	4.7nF
C337	C367	680nF
C335	C365	3.3nF

Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable: NA
 TID #: 010128
 Number: TIDA-010128 | Rev: E3
 SVN Rev: Version control disabled
 Drawn By: Avinash N
 Engineer: Anbu Mani

Designed for: Public Release
 Project Title: TIDA-010128
 Sheet Title: LMX2594R
 Assembly Variant: TIDA-01022A_Orderable
 File: TIDA-01022-E3_LMX2594R_2.SchDoc
 Contact: <http://www.ti.com/support>

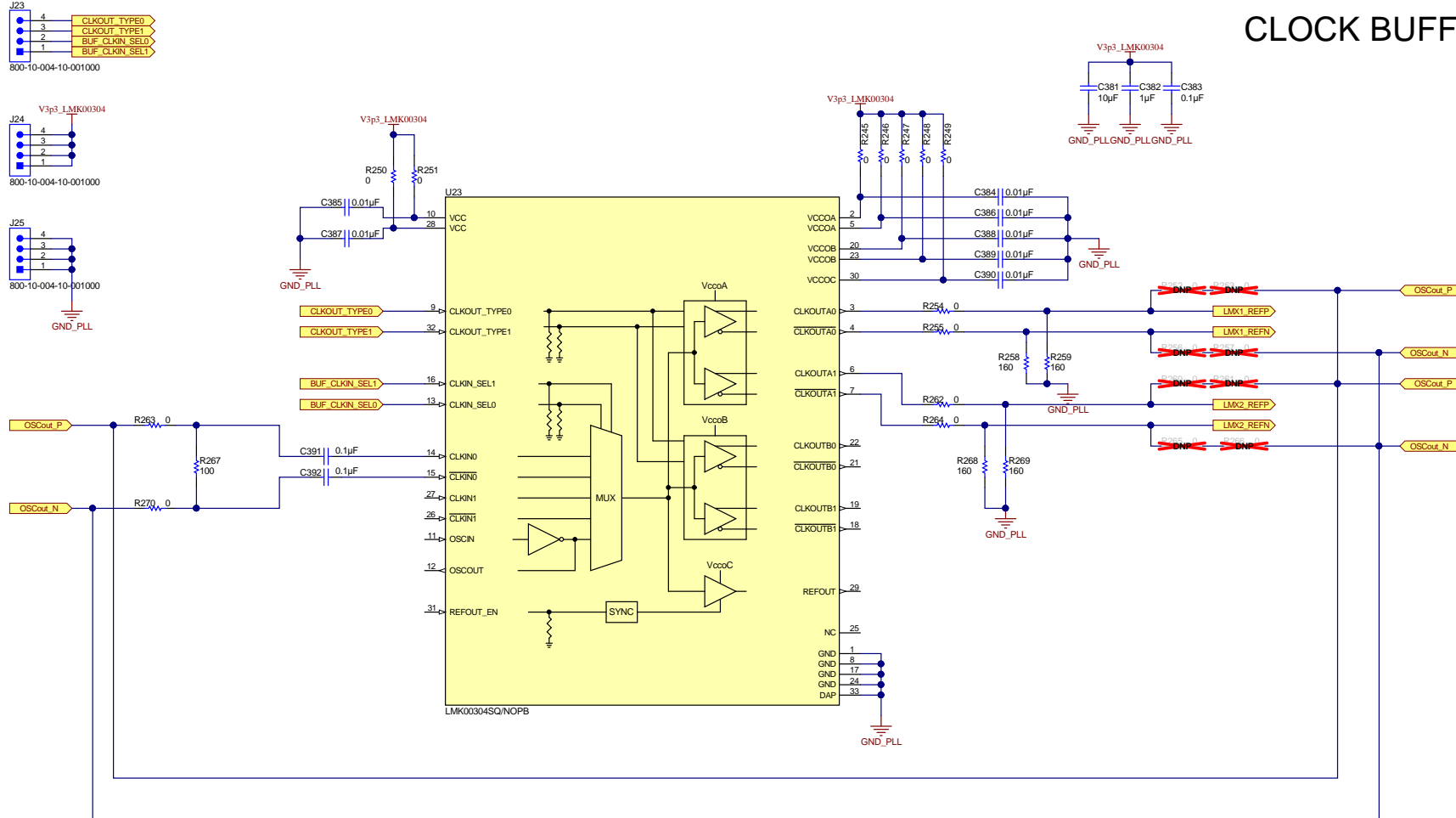
Mod. Date: 5/15/2019
 Sheet: 11 of 26
 Size: B



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CLOCK BUFFER/MUX



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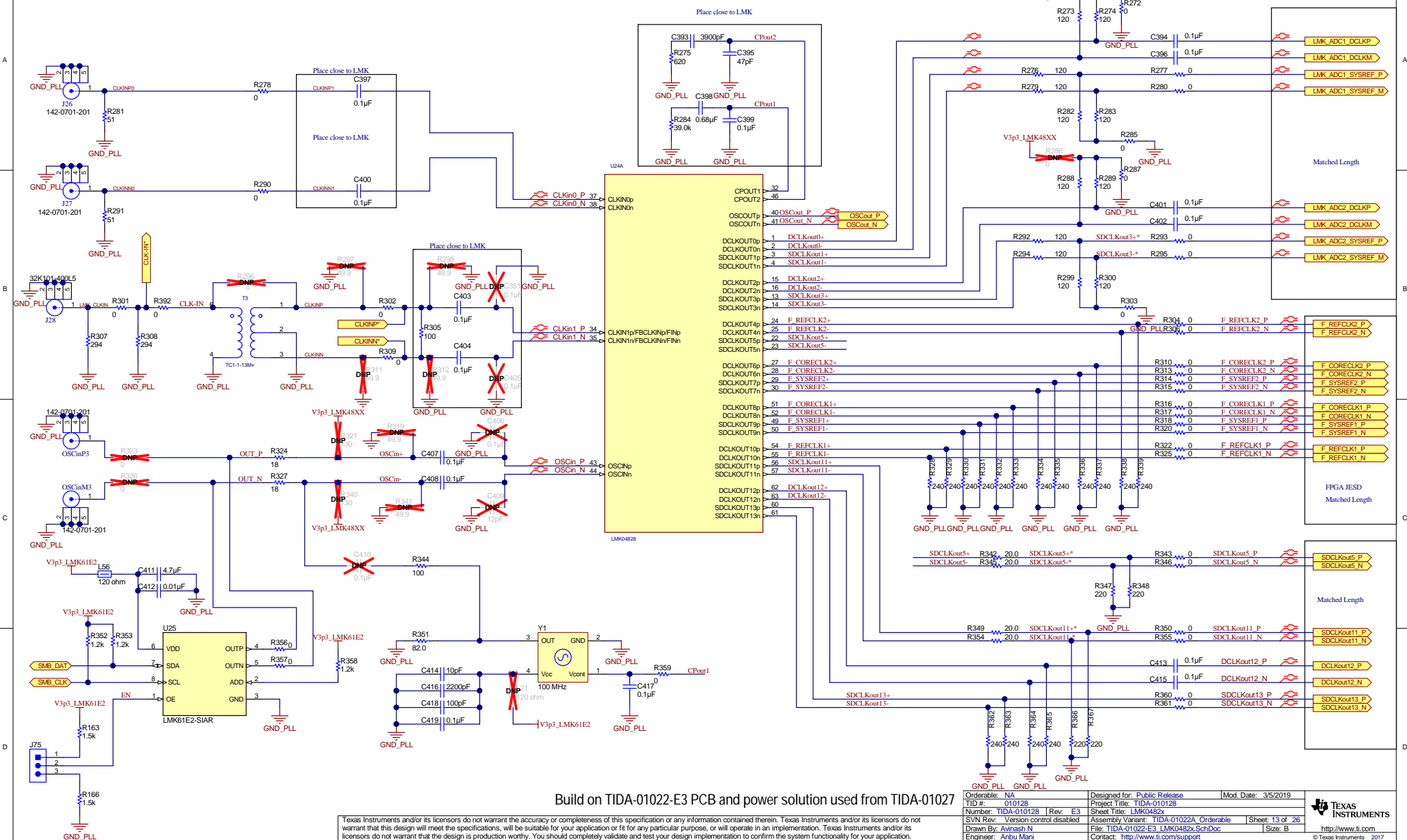
Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128 Rev: E3	Sheet Title: LMK00304_Buffer	
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A_Orderable	Sheet: 12 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_LMK00304_Buffer_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	



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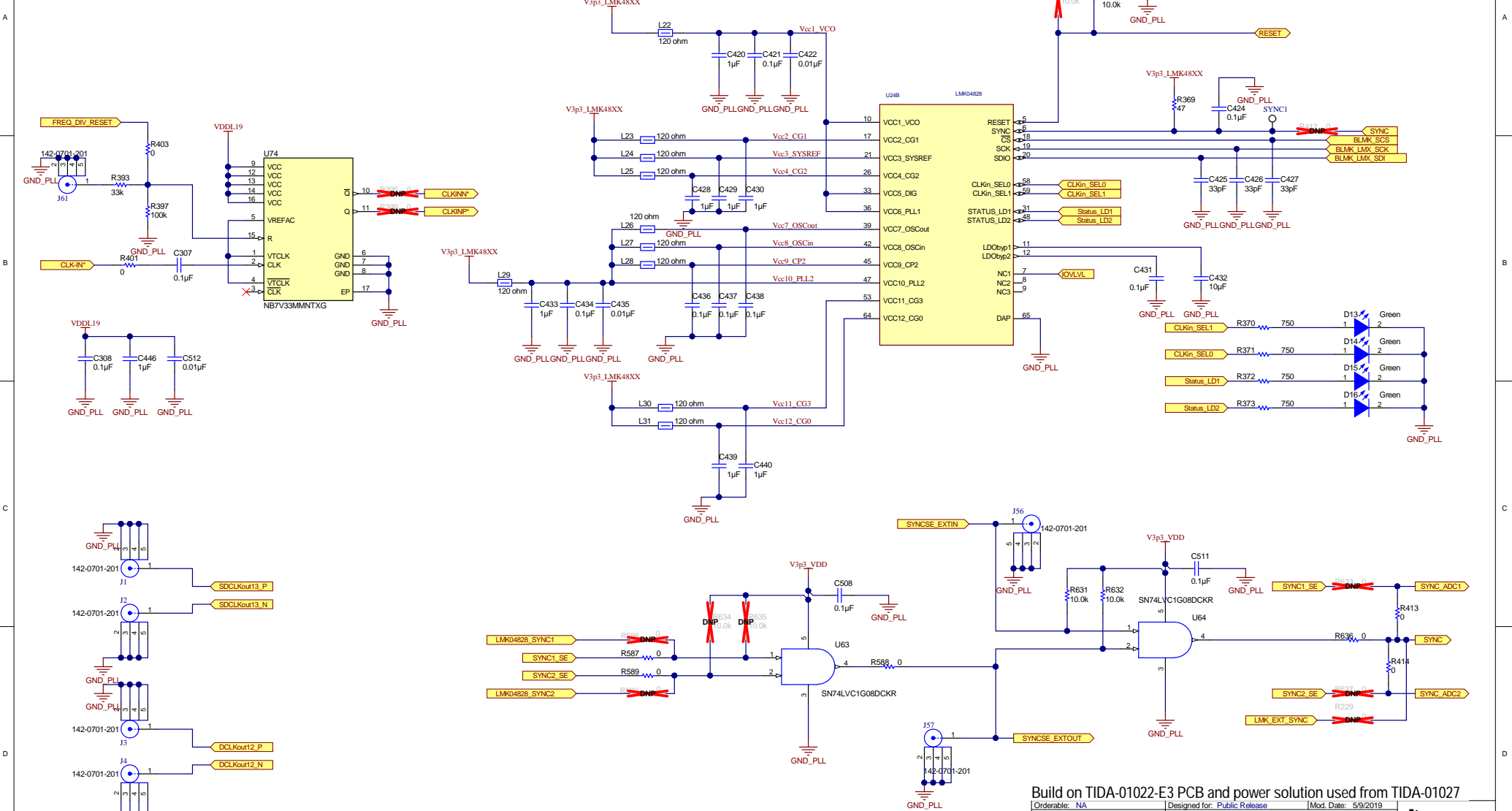
LMK 04828/32



Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable: <u>NA</u> TID #: <u>010128</u> Number: <u>TIDA-010128</u> Rev: <u>E3</u> SVN Rev: <u>Version control disabled</u> Drawn By: <u>Anvish N</u> Engineer: <u>Anbu Mani</u>		Designed for: <u>Public Release</u> Project Title: <u>TIDA-010128</u> Sheet Title: <u>UMK0482</u> Assembly Variant: <u>TIDA-01022A, Orderable</u> File: <u>TIDA-01022-E3 UMK0482_SchDoc</u> Contact: <u>http://www.ti.com/support</u>		Mod. Date: <u>3/5/2019</u> Sheet: <u>13 of 26</u> Size: <u>B</u>	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2017
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LMK 04828/32

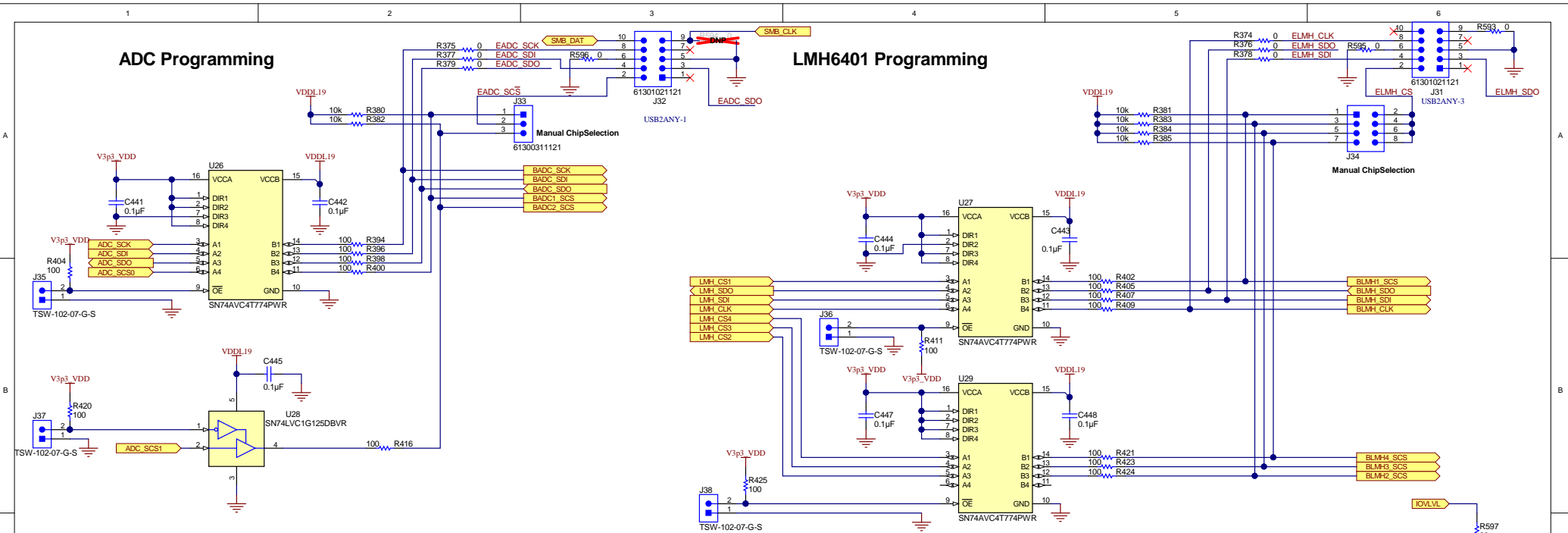


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Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

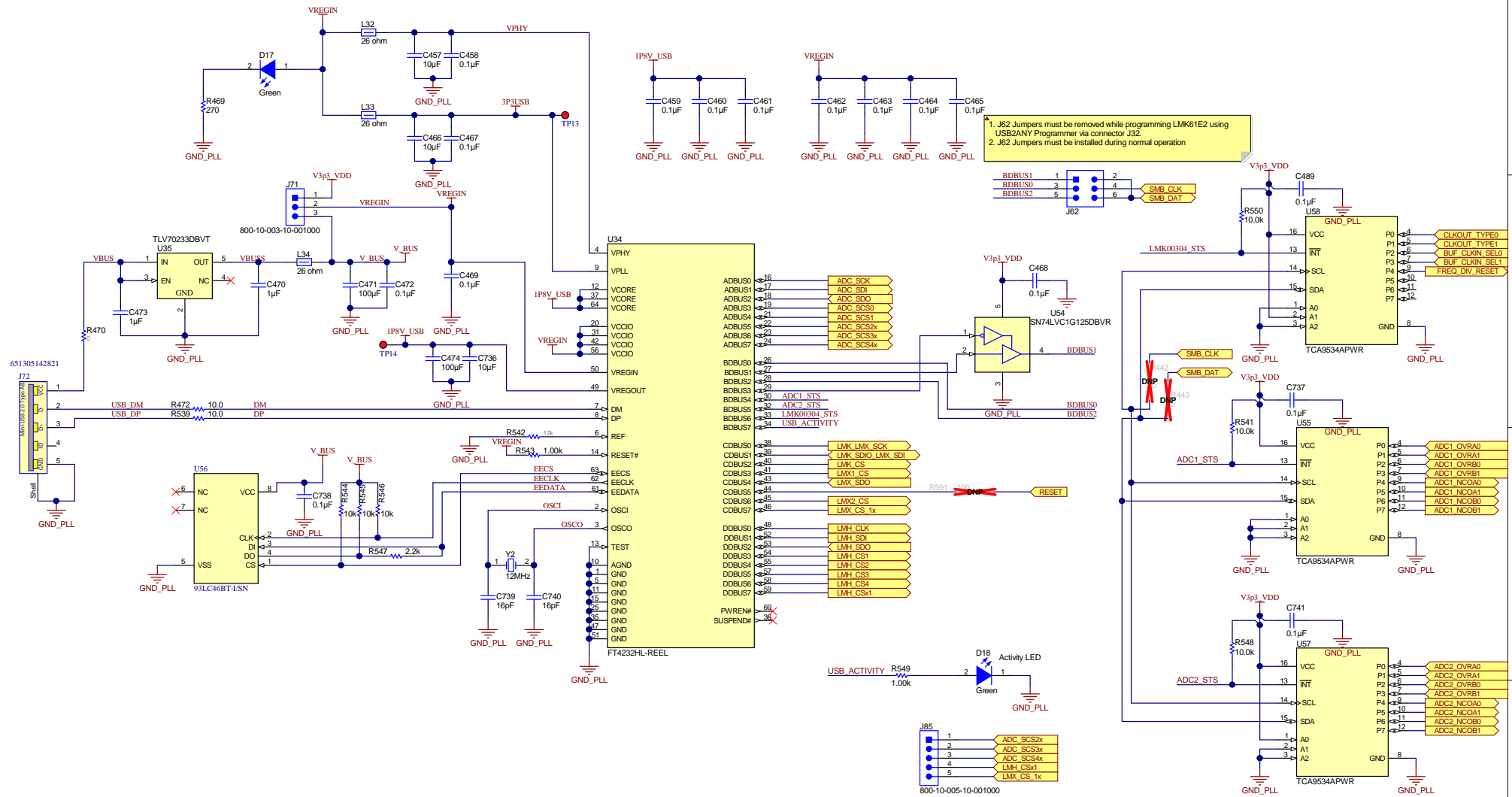
Orderable: NA	Designed for: Public Release	Mod. Date: 5/9/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128 Rev: E3	Sheet Title: LMK0482x_PWR	
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A_Orderable	Sheet: 14 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_LMK0482x_PWR_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	

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Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128	Rev: E3	Sheet Title: Programming for ADC, LMK, LMX
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A Orderable	Sheet: 15 of 15
Drawn By: Avinash N	File: TIDA-01022-E3 Programming for ADC LMK LMX	Sheet No: 15 of 15
Engineer: Anbu Mani	Contact: http://www.ti.com/support	

USB to SPI Interface



Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

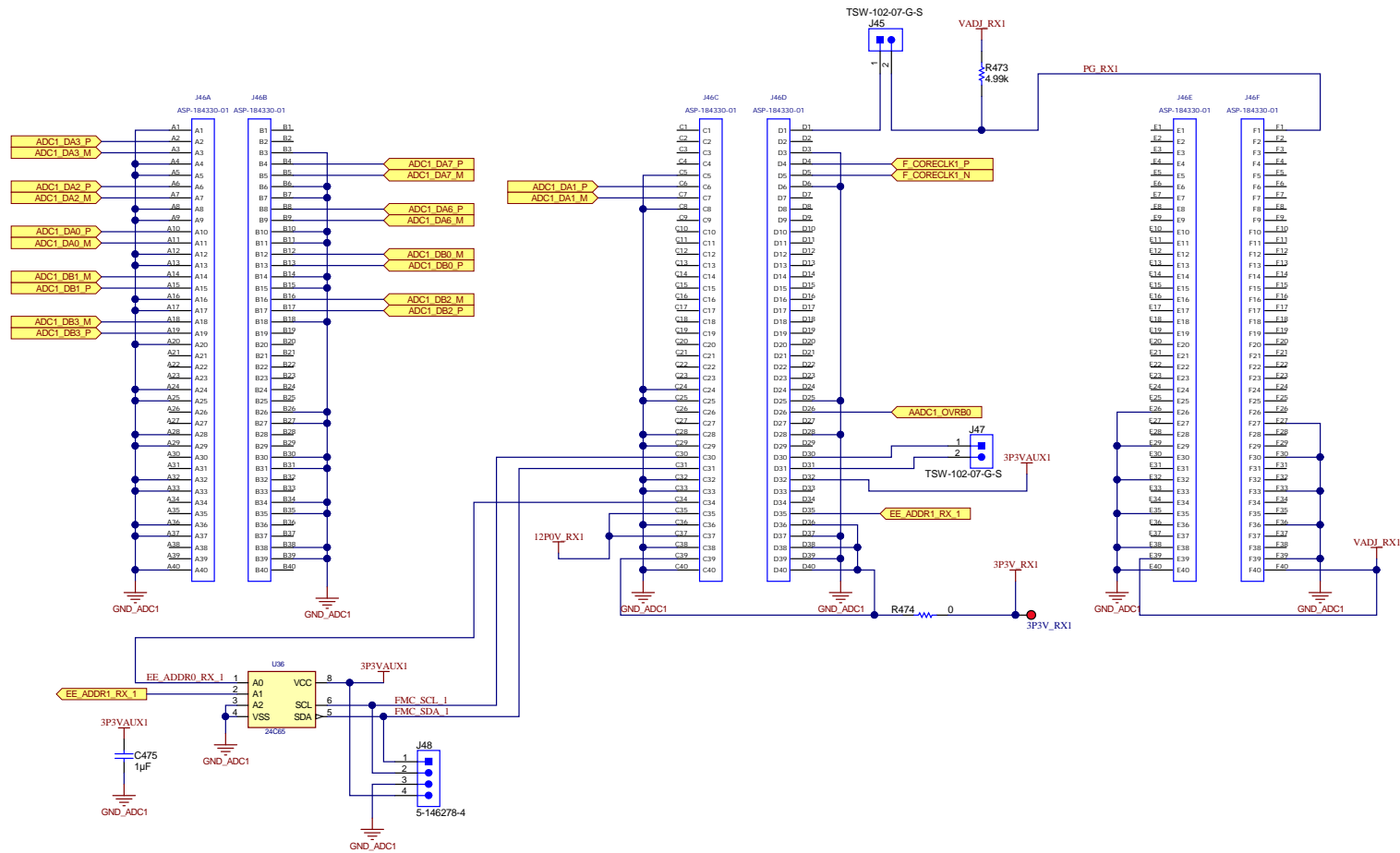
Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128	Rev: E3	Sheet Title: USB
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A_Orderable	Sheet: 16 of 26
Drawn By: Avynash N	File: TIDA-01022-E3_USB_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	



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ADC1 FMC+_1 Connector



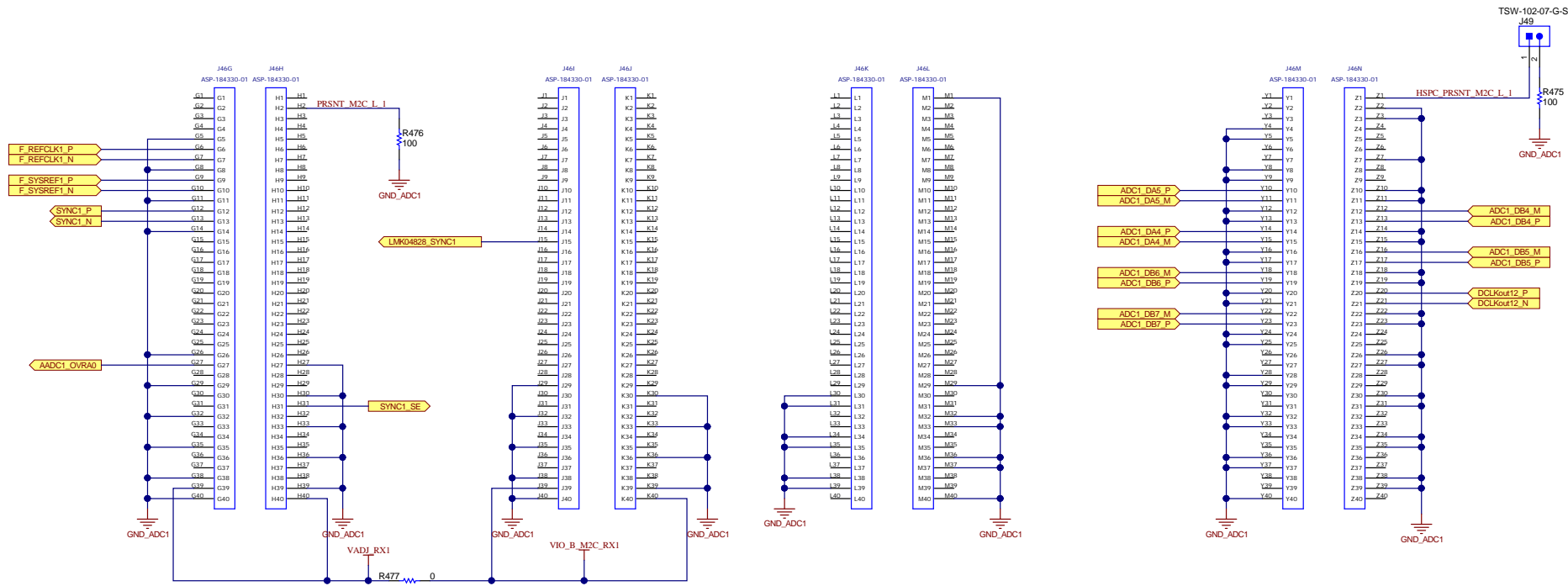
Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128	Rev: E3	Sheet Title: FMC+CONN1
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A	Orderable
Drawn By: Avinash N	File: TIDA-01022-E3_FMC+CONN1_1.SchDoc	Sheet: 17 of 26
Engineer: Anbu Mani	Contact: http://www.ti.com/support	Size: B



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ADC1 FMC+_1 Connector



Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

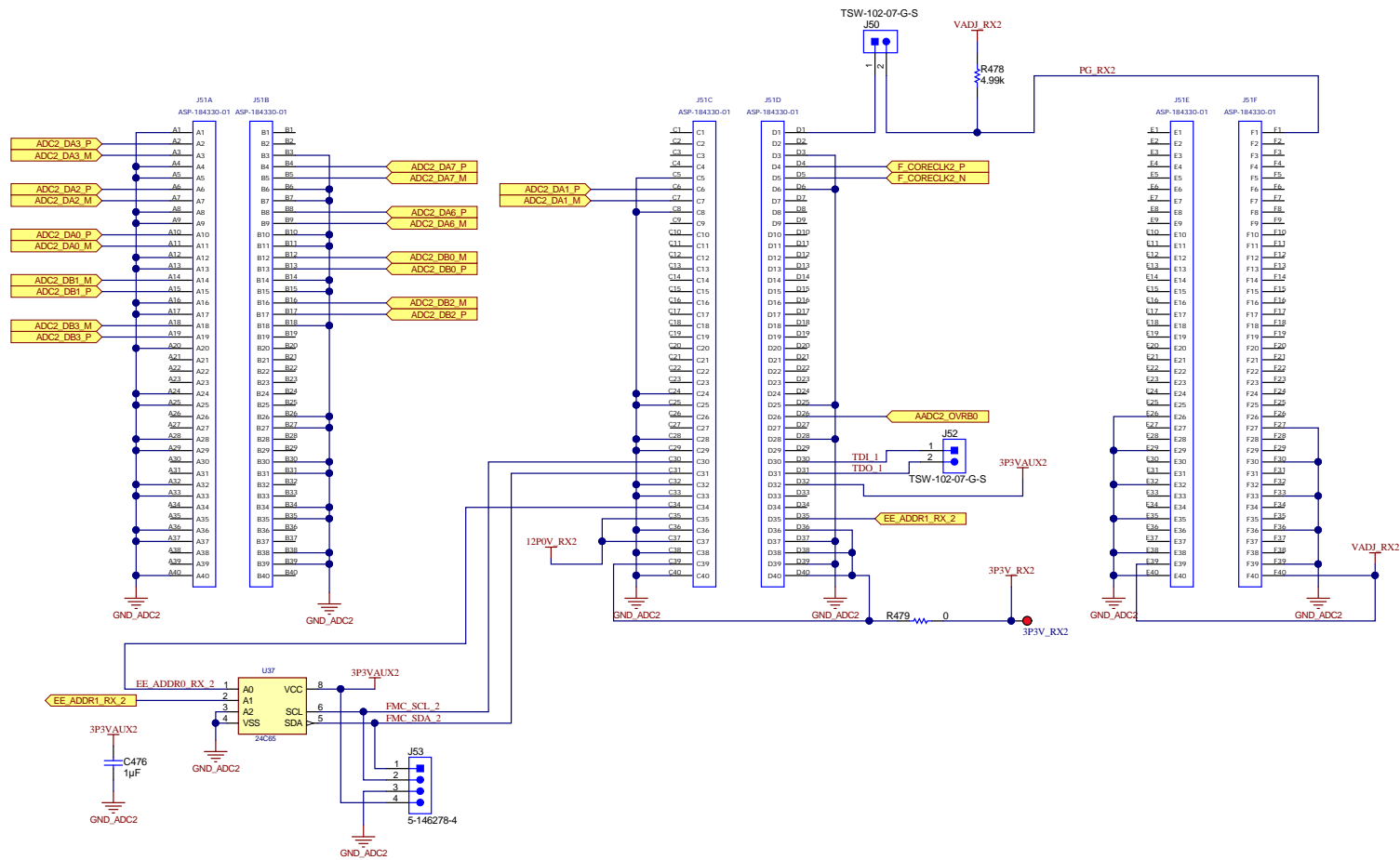
Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128 Rev: E3	Sheet Title: FMC+CONN1	
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A_Orderable	Sheet: 18 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_FMC+CONN1_2_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	



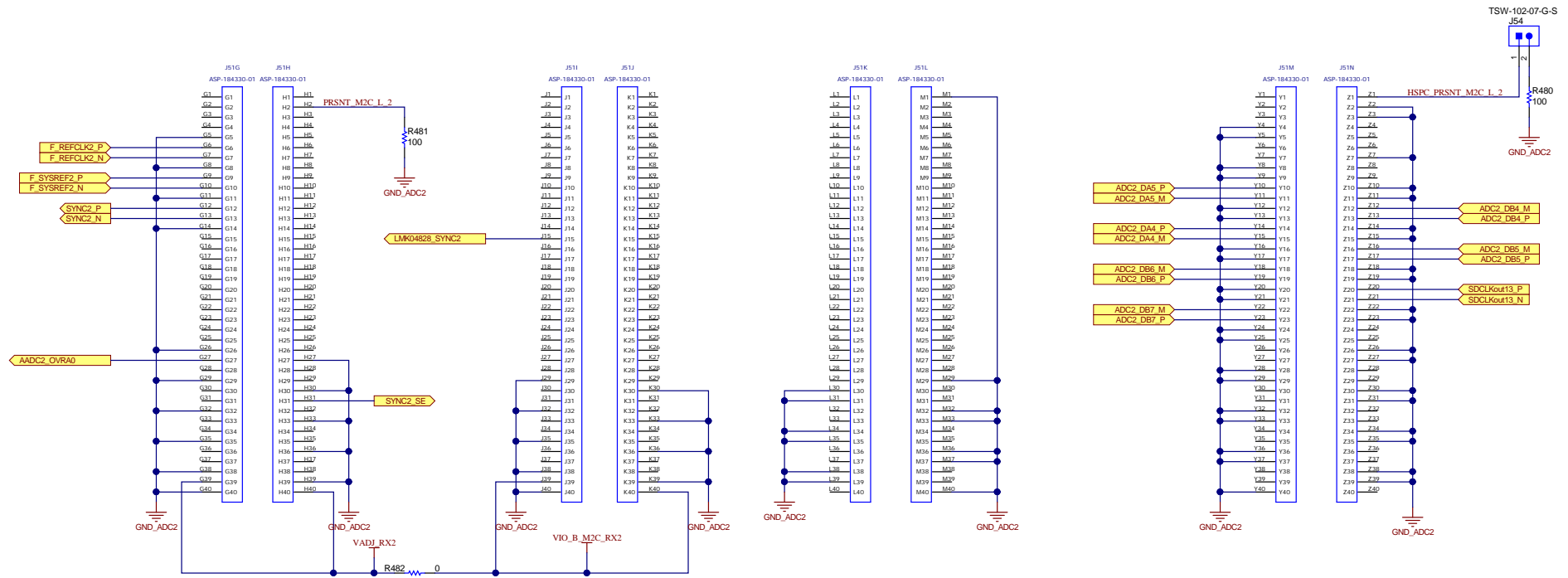
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ADC1 FMC+_2 Connector




ADC1 FMC+_2 Connector



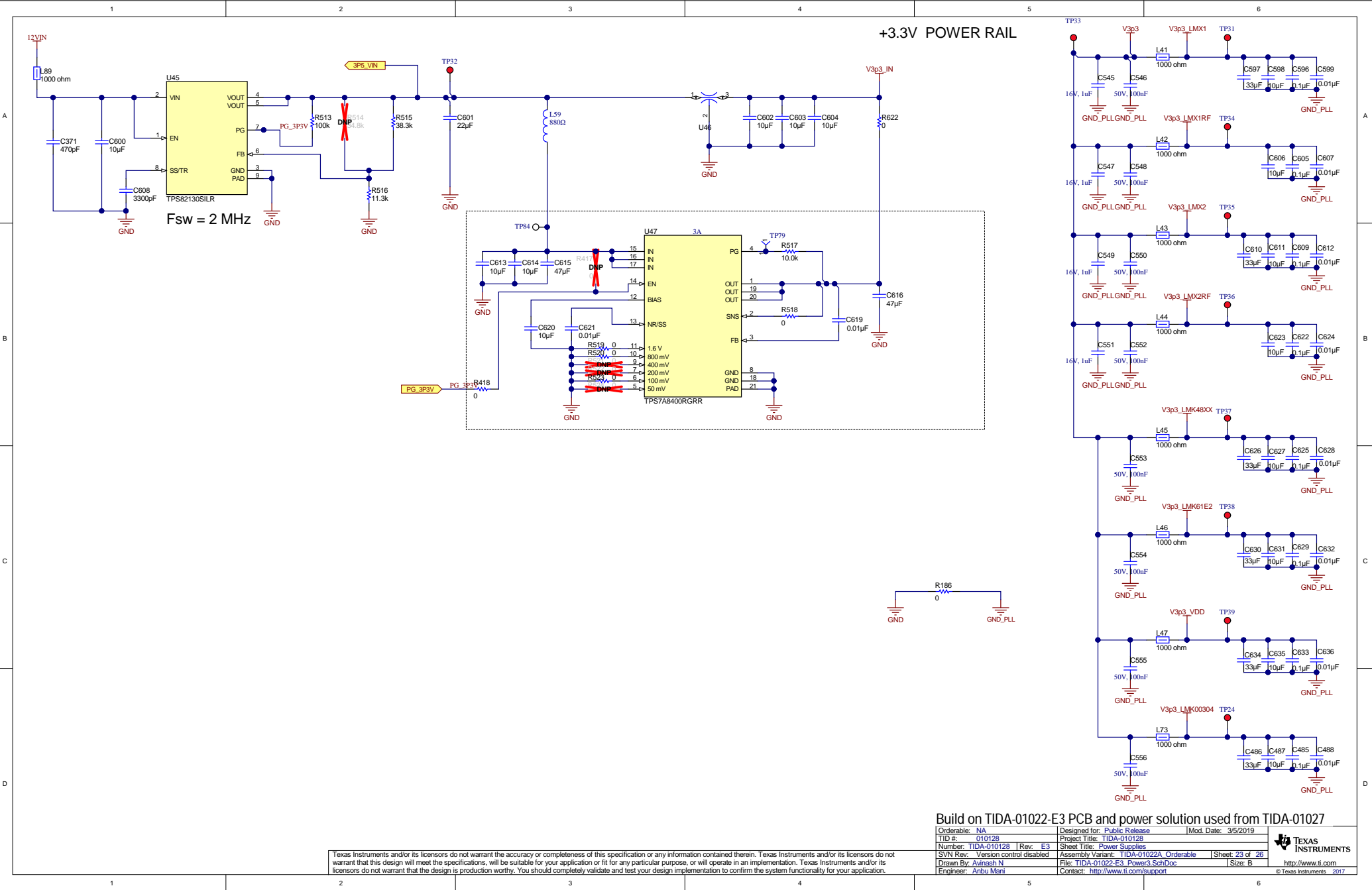
Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128 [Rev. E3]	Sheet Title: FMC+CONN2	
SVN Rev: Version controlled assembly	Assembly Variant: TIDA-01022A Orderable	Sheet: 20 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_FMC+CONN2_2_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	http://www.ti.com



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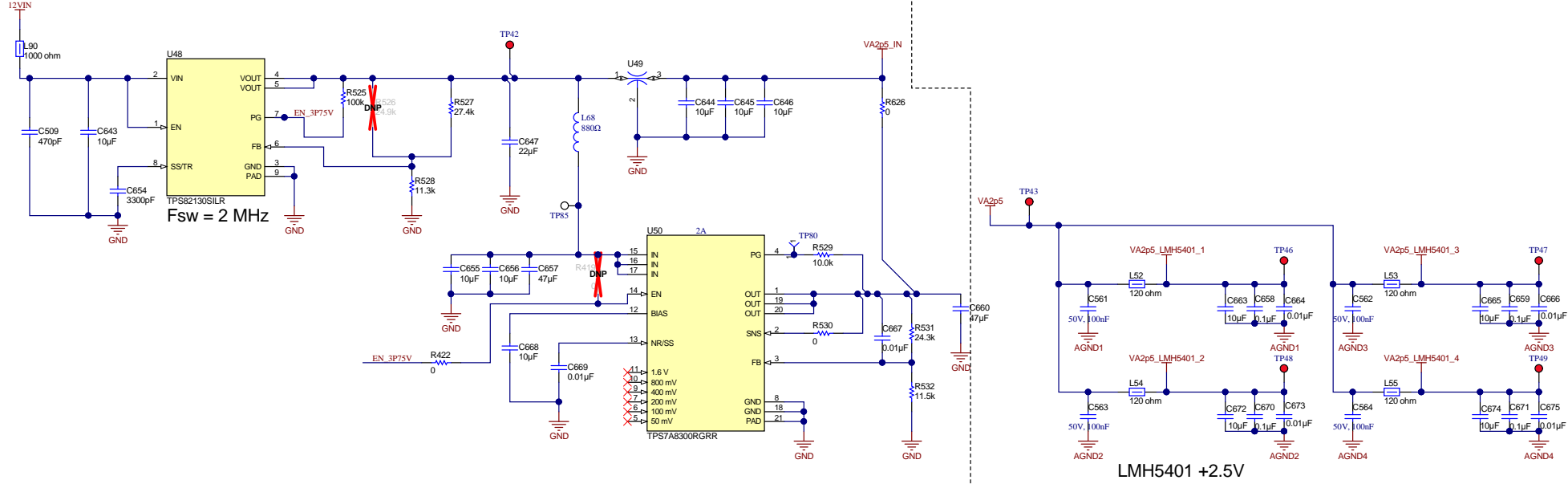


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Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128 Rev: E3	Sheet Title: Power Supplies	
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A_Orderable	Sheet: 23 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_Power3_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	





Build on TIDA-01022-E3 PCB and power solution used from TIDA-01027

Orderable: NA	Designed for: Public Release	Mod. Date: 3/5/2019
TID #: 010128	Project Title: TIDA-010128	
Number: TIDA-010128 Rev: E3	Sheet Title: Power Supplies	
SVN Rev: Version control disabled	Assembly Variant: TIDA-01022A, Orderable	Sheet: 24 of 26
Drawn By: Avinash N	File: TIDA-01022-E3_Power4_SchDoc	Size: B
Engineer: Anbu Mani	Contact: http://www.ti.com/support	



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